

Estratto da

Atti degli incontri di logica matematica Volume 6, Siena 28-31 maggio 1989.

Disponibile in rete su <http://www.ailalogica.it>

# Temporal logic of programs:

Temporal semantics, verification and specification

Extended abstract

Fred Kröger  
University of Munich

## 1. Temporal logic

Temporal logic (TL) is a branch of modal logic extending classical (propositional) logic according to the intuitive KRIPKE-like conception that propositions are interpreted over a "time-scale". TL provides logical operators for building new propositions relating the truth of propositions at certain time points.

**Example:** "A will hold at the next time point that B holds". This will be formalized by  $A \text{atnext} B$ .

### The language $\mathcal{L}_{TA}$ of propositional temporal logic

The alphabet of  $\mathcal{L}_{TA}$  consists of a denumerable set  $V$  of *atomic formulas* and the symbols  $\neg, \rightarrow, \circ, \square, \text{atnext}, (, )$ .

Formulas are inductively defined:

- Every  $v \in V$  is a formula.
- If  $A, B$  are formulas, then  $\neg A, (A \rightarrow B), \circ A, \square A, (A \text{atnext} B)$  are formulas.

We read: - "nexttime A" for  $\circ A$ ,  
- "always A" for  $\square A$ .

The following abbreviations can be introduced:

- $\wedge, \vee, \leftrightarrow, \text{true}, \text{false}$  as usual,
- $\diamond A$  for  $\neg \square \neg A$  ("sometime A").

In order to save parentheses when writing down formulas we fix the following ordering of the operators w.r.t. their binding:

$$\leftrightarrow < \rightarrow < \wedge, \vee < \text{atnext} < \neg, \circ, \square, \diamond$$

This means that  $\leftrightarrow$  binds least,  $\neg, \circ, \square, \diamond$  bind most. Outermost parentheses are always omitted.

### Semantics of $\mathcal{L}_{TA}$

A *temporal (Kripke) structure*  $K$  for  $\mathcal{L}_{TA}$  is a denumerable sequence  $\{\eta_0, \eta_1, \eta_2, \dots\}$  of mappings (*states*)

$$\eta_i: V \rightarrow \{f, t\}$$

where  $f$  and  $t$  are *truth values*. For every formula  $F$ , temporal structure  $K$  and  $i \in \mathbb{N}_0$ , the *truth value of F in state  $\eta_i$*  - denoted by  $K_i(F)$  - is defined inductively:

$$\begin{aligned}
K_i(v) &= \eta_i(v) && \text{for } v \in V, \\
K_i(\neg A) &= t && \text{iff } K_i(A) = f, \\
K_i(A \rightarrow B) &= t && \text{iff } K_i(A) = f \text{ or } K_i(B) = t, \\
K_i(\bigcirc A) &= t && \text{iff } K_{i+1}(A) = t, \\
K_i(\Box A) &= t && \text{iff } K_j(A) = t \text{ for every } j \geq i, \\
K_i(A \text{atnext } B) &= t && \text{iff } K_j(B) = f \text{ for every } j > i \text{ or} \\
&&& K_k(A) = t \text{ for the smallest } k > i \text{ with } K_k(B) = t.
\end{aligned}$$

This definition extends to the operators introduced as abbreviations, e.g.:

$$\begin{aligned}
K_i(A \wedge B) &= t && \text{iff } K_i(A) = t \text{ and } K_i(B) = t, \\
K_i(\Diamond A) &= t && \text{iff } K_j(A) = t \text{ for some } j \geq i.
\end{aligned}$$

**Remark:**  $K_i(\bigcirc A) = K_i(A \text{atnext } true)$  holds for every  $K$  and  $i$ . So, the operator  $\bigcirc$  could also be introduced as an abbreviation. The same holds for  $\Box$ :

$$\begin{aligned}
\bigcirc A &\text{ abbreviates } A \text{atnext } true, \\
\Box A &\text{ abbreviates } A \wedge \text{false atnext } \neg A.
\end{aligned}$$

**Definition:** Let  $A$  be a formula,  $F$  a set of formulas,  $K$  a temporal structure.

$$\begin{aligned}
\|_K A &\quad (A \text{ valid in } K) && \text{iff } K_i(A) = t \text{ for every } i \in \mathbb{N}_0, \\
\| A &\quad (A \text{ valid}) && \text{iff } \|_K A \text{ for every } K, \\
F \Vdash A &\quad (A \text{ follows from } F) && \text{iff } \|_K A \text{ for every } K \text{ with: } \|_K B \text{ for every } B \in F.
\end{aligned}$$

*A list of some valid formulas and rules*

$$\begin{aligned}
\neg \bigcirc A &\leftrightarrow \bigcirc \neg A \\
\Box A &\rightarrow A \\
A &\rightarrow \bigcirc A \\
\Box A &\rightarrow A \text{atnext } B \\
\Diamond \Box A &\rightarrow \Box \Diamond A \\
\Box \Box A &\leftrightarrow \Box A \\
\Box \bigcirc A &\leftrightarrow \Box A \\
\bigcirc(A \rightarrow B) &\leftrightarrow \bigcirc A \rightarrow \bigcirc B \\
\bigcirc(A \text{atnext } B) &\leftrightarrow \bigcirc A \text{atnext } \bigcirc B \\
\Box(A \wedge B) &\leftrightarrow \Box A \wedge \Box B \\
\Box A \vee \Box B &\rightarrow \Box(A \vee B) \\
\Box(A \rightarrow B) &\rightarrow (\Box A \rightarrow \Box B) \\
\Box A &\leftrightarrow A \wedge \Box A \\
A \text{atnext } B &\leftrightarrow \bigcirc(B \rightarrow A) \wedge \bigcirc(\neg B \rightarrow A \text{atnext } B) \\
A \rightarrow B &\Vdash \bigcirc A \rightarrow \bigcirc B \\
A \rightarrow B &\Vdash \Box A \rightarrow \Box B \\
A &\Vdash \Box B \rightarrow \Box(A \wedge B) \\
\Box A \rightarrow B &\Vdash \Box A \rightarrow \Box B
\end{aligned}$$

*Some further operators*

Based on the temporal operators of  $\mathcal{L}_{TA}$ , many other operators can be introduced, e.g.:

$$\begin{aligned}
A \text{ unless } B &\text{ for } B \text{atnext } (A \rightarrow B), \\
A \text{ while } B &\text{ for } \neg B \text{atnext } (A \rightarrow \neg B), \\
A \text{ before } B &\text{ for } \neg B \text{atnext } (A \vee B).
\end{aligned}$$

The informal meaning of these operators is:

*A unless B:* "If there is a following state in which  $B$  holds then  $A$  holds up to that point or else  $A$  holds permanently",  
*A while B:* "A holds as long as  $B$  holds",  
*A before B:* "If  $B$  holds sometime in the future then  $B$  holds before that".

*The formal system  $\Sigma_{TA}$*

The following axiomatization of TL can be given:

$$\begin{aligned}
\text{Axioms:} & \quad - \text{ all "classically valid" formulas (e.g. } \bigcirc A \rightarrow \bigcirc A) \\
& \quad - \neg \bigcirc A \leftrightarrow \bigcirc \neg A \\
& \quad - \bigcirc(A \rightarrow B) \rightarrow (\bigcirc A \rightarrow \bigcirc B) \\
& \quad - \Box A \rightarrow A \wedge \Box A \\
& \quad - \bigcirc \Box \neg B \rightarrow A \text{atnext } B \\
& \quad - A \text{atnext } B \leftrightarrow \bigcirc(B \rightarrow A) \wedge \bigcirc(\neg B \rightarrow A \text{atnext } B)
\end{aligned}$$

$$\begin{aligned}
\text{Rules:} & \quad (\text{mp}) \quad A, A \rightarrow B \vdash B \\
& \quad (\text{nex}) \quad A \vdash \bigcirc A \\
& \quad (\text{ind}) \quad A \rightarrow B, A \rightarrow \bigcirc A \vdash A \rightarrow \Box B
\end{aligned}$$

The notion of *derivability* (w.r.t.  $\Sigma_{TA}$ ) of a formula  $A$  from a set  $F$  of formulas - denoted by  $F \vdash A$  - is defined as usual.

**Soundness theorem:** *If  $F \vdash A$  then  $F \Vdash A$ .*

**Deduction theorem:** *If  $F \cup \{A\} \vdash B$  then  $F \vdash \Box A \rightarrow B$ .*

The proofs of these theorems are straightforward.

*Completeness*

The most general completeness assertion

$$\text{If } F \Vdash A \text{ then } F \vdash A$$

does not hold in TL. A simple counterexample is given by the infinite set

$$F = \{A \rightarrow B, A \rightarrow \bigcirc B, A \rightarrow \bigcirc \bigcirc B, A \rightarrow \bigcirc \bigcirc \bigcirc B, \dots\}.$$

$F \Vdash A \rightarrow \Box B$  holds, but  $A \rightarrow \Box B$  cannot be derived from  $F$ . (A formal derivation can use only finitely many premises from  $F$ .)

However, it is possible to prove

$$\text{If } F \Vdash A \text{ then } F \vdash A \text{ for finite sets } F \text{ of formulas.}$$

For proving this it is sufficient to show:

**Completeness theorem:** *If  $F \Vdash A$  then  $F \vdash A$ .*

We give a proof outline for this theorem. The basic idea follows the classical HENKIN-method which, however, has now to be modified carefully. (All sets of formulas which are constructed in this method have to be finite.) A finite set  $F = \{A_1, \dots, A_n\}$ ,  $n \geq 0$ , of formulas is called

- *consistent* if it is empty, or  $n > 0$  and  $\neg(A_1 \wedge \dots \wedge A_n)$  is not derivable in  $\Sigma_{TA}$ ,
- *satisfiable* if there is a temporal structure  $K$  such that  $K_0(A_j) = t$  for all  $j = 1, \dots, n$ .

To show the completeness theorem it is sufficient to show the following

**Satisfiability theorem:** *Every finite and consistent set of formulas is satisfiable.*

(From this the completeness theorem is deduced as in the classical case.) We illustrate the proof idea for this theorem by an example. Let  $A$  be the formula  $(v_1 \rightarrow v_2) \rightarrow \Box v_3$ ,  $B$  the

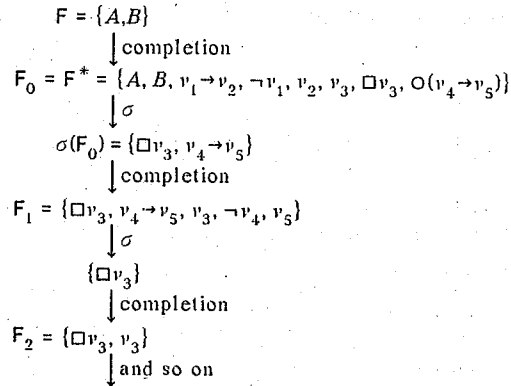
formula  $v_2 \rightarrow O(v_4 \rightarrow v_5)$  and  $F = \{A, B\}$ . ( $v_1, \dots, v_5 \in V$ .) In order to "make  $A$  and  $B$  true" in  $\eta_0$  we build a (finite, consistent) *completion*  $F^*$  of  $F$ .  $F^*$  consists of sub-formulas of  $F$  in such a way that if these formulas become true in  $\eta_0$  then so will  $A$  and  $B$ . For technical reasons we include  $A$  and  $B$  themselves into  $F^*$  and obtain e.g.:

$$F^* = \{A, B, v_1 \rightarrow v_2, \neg v_1, v_2, v_3, \Box v_3, O(v_4 \rightarrow v_5)\}.$$

The formulas  $\Box v_3$  and  $O(v_4 \rightarrow v_5)$  - considered in  $\eta_0$  - have some "influence" on the states  $\eta_1, \eta_2, \dots$ . We apply a mapping  $\sigma$  on  $F^*$  which produces a set  $\sigma(F)$  of formulas (which is again finite and consistent) such that if these formulas are true in  $\eta_1$  then  $K_0(\Box v_3) = t$  and  $K_0(O(v_4 \rightarrow v_5)) = t$ . We have

$$\sigma(F^*) = \{\Box v_3, v_4 \rightarrow v_5\}.$$

Continuing in the same way we obtain the following sequence  $F_0, F_1, F_2, \dots$  of formula sets:

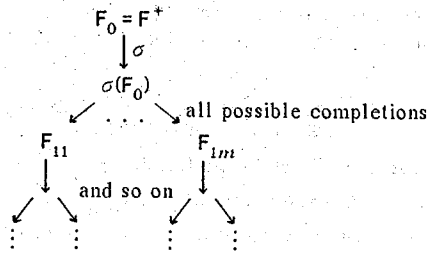


From this (over-simplified) construction we find  $K = \{\eta_0, \eta_1, \eta_2, \dots\}$  by

$$\eta_i(v) = t \text{ iff } v \in F_i \text{ for every } v \in V, i \in \mathbb{N}_0.$$

It is clear that with this  $K$  we get  $K_0(A) = K_0(B) = t$ .

There is, however, a problem in this construction. Suppose, some  $F_j$  contains the formulas  $\neg \Box v$  and  $v$  ( $v \in V$ ). Constructing  $F_{j+1}$ , we get  $\neg \Box v \in F_{j+1}$  and it may also happen that we obtain again  $v \in F_{j+1}$ . The same may hold for all the next steps which would end up in  $\eta_i(v) = t$  for all  $i \geq j$  and therefore  $K_j(\neg \Box v) = f$ . This, of course, should not be the case. The solution of this problem is found if we observe the fact that in every step, usually more than one completion is possible. The actual construction is to consider all these possibilities resulting not in a sequence  $F_0, F_1, F_2$  of formula sets but in a tree of the form



It can be shown then that there is a path through this tree (yielding  $\{\eta_0, \eta_1, \eta_2, \dots\}$  as before) which avoids the above-mentioned problem. The proof runs by contradiction: Assume there is a node  $N_i$  in the tree such that for some formula  $A$ , both  $\neg \Box A$  and  $A$  are contained in  $N_i$  and in all descendant nodes denoted by  $N_2, N_3, \dots$ . If  $N_i = \{B_1, \dots, B_k\}$  let  $\hat{N}_i$  denote the formula  $B_1 \wedge \dots \wedge B_k$ . It is easy to see that there are only finitely many different  $N_i$ , say,  $N_1, \dots, N_n$ . Let  $C = \hat{N}_1 \vee \dots \vee \hat{N}_n$ . Then one can show:

- (1)  $\vdash \hat{N}_1 \rightarrow C$
- (2)  $\vdash C \rightarrow \Box C$
- (3)  $\vdash C \rightarrow A$

From (2) and (3),  $\vdash C \rightarrow \Box A$  follows with rule (ind). Together with (1) we obtain  $\vdash \hat{N}_1 \rightarrow \Box A$  which leads to a contradiction because of  $\neg \Box A \in N_1$ .

### Induction principles

In applications, formulas of the kind

- $$\begin{array}{l}
 A \rightarrow \Box B, \\
 A \rightarrow B \text{ atnext } C, \\
 A \rightarrow B \text{ unless } C, \\
 \vdots \\
 A \rightarrow \Diamond B
 \end{array}$$

are of particular interest. We give some basic proof rules ("induction principles") for proving them:

For  $A \rightarrow \Box B$ , there is already a rule given by  $\Sigma_{TA}$ :

$$\text{(ind)} \quad A \rightarrow B, A \rightarrow \Box A \vdash A \rightarrow \Box B.$$

Some variants of (ind) are:

$$\text{(ind')} \quad A \rightarrow \Box A \vdash A \rightarrow \Box A,$$

$$\text{(ind'')} \quad A \rightarrow B, B \rightarrow \Box B \vdash A \rightarrow \Box B.$$

For  $A \rightarrow B \text{ atnext } C$  there is the rule

$$\text{(indatnext)} \quad A \rightarrow O(C \rightarrow B) \wedge O(\neg C \rightarrow A) \vdash A \rightarrow B \text{ atnext } C.$$

For **unless**, **while**, **before** similar rules can be given. For  $A \rightarrow \Diamond B$  no such principle (in propositional TL) exists.

### First-order temporal logic (with equality)

$\mathcal{L}_{TA}$  can be extended to a first-order version  $\mathcal{L}_{TP}$  by additional language features:

- variables (partitioned into *global* and *local* variables),
- function and predicate symbols, the equality symbol =,
- quantification  $\forall$  (and  $\exists$ ), allowed only over global variables.

The semantics of  $\mathcal{L}_{TP}$  is given as in classical logic; in each state  $\eta_i$ , a value  $\eta_i(x)$  is assigned to variables  $x$ . For global variables  $x$  we request:

$$\eta_i(x) = \eta_j(x) \text{ for all } i, j.$$

In the axiomatization we can add some further axioms and rules, e.g.:

- $\forall x A \rightarrow A_x(t)$  if  $A_x(t)$  has no other occurrences of local variables in the scope of a temporal operator than  $A$
- $\forall x \Box A \rightarrow \Box \forall x A$

- $A \rightarrow OA$  if  $A$  does not contain local variables
- $x = x$
- $x = y \rightarrow (A \rightarrow A_x(y))$  if  $A$  does not contain temporal operators
- $A \rightarrow B \vdash A \rightarrow \forall x B$  if there is no free occurrence of  $x$  in  $A$

$(A_x(t))$  denotes the effect of substituting  $t$  for  $x$  in  $A$  after appropriate renaming of bound variables of  $A$  which occur in  $t$ . Soundness is again easily proved. The formal system is, however, not complete. In fact, the logic can not be axiomatized completely. The strongest incompleteness result (to our knowledge) is [3]:

- If the first-order language contains at least two binary function symbols then the logic is incomplete.

The idea to prove this is to interpret Peano arithmetic in the logic: 0 and the successor function are definable, the two function symbols can be axiomatized to be addition and multiplication.

### The well-founded ordering principle

Let  $TL^{wf}$  be a first-order temporal logic containing

- variables ranging over a well-founded set  $Z$ ,
- a binary predicate symbol  $\leq$  interpreted by the well-founded ordering on  $Z$ .

In  $TL^{wf}$  we can formulate a basic proof rule for deriving formulas of the form  $A \rightarrow \Diamond B$ :

(wfo)  $A(x) \rightarrow \Diamond(B \vee \exists x'(x' < x \wedge A(x'))) \vdash \exists x A(x) \rightarrow \Diamond B$  if  $B$  does not contain  $x$ .

### Extensions

Temporal logic is investigated (and applied) in the literature in many other forms. We only give a few keywords:

- branching time logic: The underlying Kripke structure is not totally but only partially ordered;
- other modifications of the "time scale": not discrete, finite;
- more powerful temporal operators, in particular "past" operators;
- interval logic: Interpretation of formulas not over "time points" but over "time intervals";
- multi-dimensionality: Further dimensions besides "time";
- probabilistic additions: Truth values varying over the real interval  $[0,1]$  interpreted as probabilities.

## 2. Programs and their temporal semantics

Temporal logic is applicable to many kinds of (mainly imperative) programs, in particular parallel programs. Here we illustrate this applicability by a very simple class of parallel programs containing, e.g., the following (skeleton of a) program  $\Pi$ :

```

initial  $ex=true \wedge bf=0 \wedge be=n \wedge n>0$ ;
cobegin loop  $\alpha_{00} : \dots$  ;
      :
       $\alpha_{0k_0} : \dots$  ;
       $\alpha_1 : \text{await } be>0 \text{ then } be := be-1$ ;
       $\alpha_2 : \text{await } ex=true \text{ then } ex := false$ ;
       $\alpha_{30} : \dots$  ;
      :
       $\alpha_{3k_3} : \dots$  ;
       $\alpha_4 : ex := true$ ;
       $\alpha_5 : bf := bf+1$ 
end,
loop  $\beta_0 : \text{await } bf>0 \text{ then } bf := bf-1$ ;
   $\beta_1 : \text{await } ex=true \text{ then } ex := false$ ;
   $\beta_{20} : \dots$  ;
  :
   $\beta_{2l_2} : \dots$  ;
   $\beta_3 : ex := true$ ;
   $\beta_4 : bf := bf+1$ ;
   $\beta_{50} : \dots$  ;
  :
   $\beta_{5l_5} : \dots$ 
end
coend

```

The first line defines (under the keyword **initial**) the *initial condition* of  $\Pi$ . The two *cyclic loops* between **cobegin** and **coend**, each of them enclosed by **loop** and **end**, are thought to be computed in parallel. The loops consist of sequences of labeled *atomic statements* (separated by ";"), the  $\alpha$ 's and  $\beta$ 's being unique *labels* of the statements. The statements (labeled by)  $\alpha_4, \alpha_5, \beta_3, \beta_4$  are *assignments* like in usual imperative programming languages; the statements  $\alpha_1, \alpha_2, \beta_0, \beta_1$  manage some *synchronization* of the two parallel components of  $\Pi$ .

### The general form of a program (of our sample class)

The programs we consider are of the form

```

initial  $R$ ;
cobegin  $\Pi_1, \dots, \Pi_p$  coend      ( $p \geq 1$ )

```

where

- every  $\Pi_i$  is of the form
 

```

loop  $\alpha_0^{(i)} : a_0^{(i)} ; \alpha_1^{(i)} : a_1^{(i)} ; \dots ; \alpha_{m_i}^{(i)} : a_{m_i}^{(i)}$  end

```
- every  $\alpha_j^{(i)}$  is of the form
 

```

 $a := t$  or
await  $B$  then  $a := t$ 

```

 ( $a$  is a *program variable*,  $t$  is a *term*, and  $B$  is a *formula*),
- all  $\alpha_j^{(i)}$  in  $\Pi$  are pairwise different.

For further use we let  $\mathfrak{M}_{\Pi_i} = \{\alpha_0^{(i)}, \dots, \alpha_{m_i}^{(i)}\}$  and  $\mathfrak{M}_{\Pi} = \bigcup_{i=1}^p \mathfrak{M}_{\Pi_i}$ .

### Modelling the parallel computation of $\Pi_1, \dots, \Pi_p$

The (interleaving) model of computation of a parallel program of the described form is informally given as follows:

- The computation is a sequence of interleaved executions of the atomic statements (beginning with some  $\alpha_0^{(i)}$ ):  
 $\dots; \alpha_{j_1}^{(i_1)}; \alpha_{j_2}^{(i_2)}; \alpha_{j_3}^{(i_3)}; \dots$
- The relative ordering of the  $\alpha_j^{(i)}$  for every fixed  $i$  is maintained;
- Any **await**  $B$  **then** ... may occur as the next executed statement only if  $B$  holds.

**Example:** A possible execution of the program

```
initial a=0 ∧ b=0;
cobegin loop  $\alpha_0: a := a+1; \alpha_1: \text{await } b \neq 0 \text{ then } a := a \times b; \alpha_2: a := a+3 \text{ end,}$ 
loop  $\beta_0: a := 2 \times a; \beta_1: \text{await } a \neq 1 \text{ then } b := 2 \times b; \beta_2: b := b+1 \text{ end}$ 
coend
```

is as follows:

action	values of $a, b$
	initially 0 0
$\alpha_0: a := a+1$	1 0
$\beta_0: a := 2 \times a$	2 0
$\beta_1: \text{await } a \neq 1 \text{ then } b := 2 \times b$	2 0
$\beta_2: b := b+1$	2 1
$\alpha_1: \text{await } b \neq 0 \text{ then } a := a \times b$	2 1
$\vdots$	

Another possible beginning of an execution is:

action	values of $a, b$
	initially 0 0
$\beta_0: a := 2 \times a$	0 0
$\alpha_0: a := a+1$	1 0

This latter situation illustrates a typical problem of parallel programming: After the execution of  $\beta_0$  and  $\alpha_0$  no one of the two parallel components can proceed (the program is in a *deadlock*) since the "await-conditions" of both  $\alpha_1$  and  $\beta_1$  are false then.

We now define the formal notions covering these intuitive ideas.

**Definition:** A *program state* is a triple  $\eta = (\mu, R, \lambda)$  with:

- $\mu$  assigns a value  $\mu(a)$  to every program variable,
- $R = \{\alpha_{j_1}^{(i_1)}, \dots, \alpha_{j_p}^{(i_p)}\}$  ( $\alpha_{j_i}^{(i)}$  is the "next statement in  $\Pi_i$ "),
- $\lambda \in R \cup \{\text{NIL}\}$  (next statement to be executed).

An *execution sequence* is an infinite sequence  $\mathbf{W}_\Pi = \{\eta_0, \eta_1, \eta_2, \dots\}$  of program states with:

- $R$  "is true in  $\eta_0 = (\mu_0, R_0, \lambda_0)$ ",
- $R_0 = \{\alpha_0^{(1)}, \dots, \alpha_0^{(p)}\}$ ,
- if  $\eta_k = (\mu, R, \alpha_j^{(i)})$  then  $\eta_{k+1} = (\mu', R', \alpha')$  with
  - $R' = (R \setminus \{\alpha_j^{(i)}\}) \cup \{\alpha_{j \oplus 1}^{(i)}\}$  (where  $\oplus$  denotes addition modulo  $m_i + 1$ ),
  - $\mu' = \mu$  changed according to  $\alpha_j^{(i)}$ ,
- if  $\eta_k = (\mu, R, \text{NIL})$  then  $\alpha_{j_i}^{(i)}$  is **await**  $B_i$  **then** ... for  $i=1, \dots, p$ ,  $B_i$  "is false in  $\eta_k$ ", and  $\eta_{k+1} = \eta_k$ .

**Example:** In the above deadlock example we have e.g.:

	$\mu(a)$	$\mu(b)$	R	$\lambda$
$\eta_0:$	0	0	$\alpha_0, \beta_0$	$\beta_0$
$\eta_1:$	0	0	$\alpha_0, \beta_1$	$\alpha_0$
$\eta_2:$	1	0	$\alpha_1, \beta_1$	NIL
$\eta_3:$	1	0	$\alpha_1, \beta_1$	NIL
and so on				

### Temporal semantics of a program $\Pi$

The basic idea of describing the "behaviour" (the *semantics*) of a program  $\Pi$  within temporal logic is to specialize the general temporal structures of TL to execution sequences of programs. This can be performed by further axioms (and one rule) which are called *program axioms* and build the *temporal semantics* of  $\Pi$ : They describe (axiomatically) all possible execution sequences of  $\Pi$ .

The language  $\mathcal{L}_{T\Pi}$  for this purpose is a first-order temporal language  $\mathcal{L}_{TP}$  with:

- program variables of  $\Pi$  as local variables,
- other variables as global variables,
- predicate and function symbols of  $\Pi$
- (additional) atomic formulas:
  - $\alpha$  for every  $\alpha \in \mathfrak{M}_\Pi$  ( $\alpha$  true in  $\eta = (\mu, R, \lambda)$  iff  $\alpha = \lambda$ ),
  - $at\alpha$  for every  $\alpha \in \mathfrak{M}_\Pi$  ( $at\alpha$  true in  $\eta = (\mu, R, \lambda)$  iff  $\alpha \in R$ ),
  - $nil$  ( $nil$  true in  $\eta = (\mu, R, \lambda)$  iff  $\lambda = \text{NIL}$ ),
- abbreviation:
  - $start_\Pi$  for  $at\alpha_0^{(1)} \wedge \dots \wedge at\alpha_0^{(p)} \wedge R$

The program axioms for our kind of programs are:

- (B1)  $start_\Pi \rightarrow \Box A \vdash A$  ("start $_\Pi$  holds in the initial state")
- (B2)  $nil \wedge A \rightarrow \Box(nil \wedge A)$  ("If *nil* then nothing is changed")
- (PI1)  $\alpha \rightarrow \neg \alpha'$  for  $\alpha \neq \alpha'$  ("No two actions execute at the same time")
- (PI2)  $\alpha \rightarrow at\alpha$  ("An action may only execute if it is ready to")
- (PI3)  $at\alpha_j^{(i)} \rightarrow \neg at\alpha_k^{(i)}$  for  $j \neq k, j, k = 0, \dots, m_i, i = 1, \dots, p$  ("In every  $\Pi_i$ , no two actions are ready to execute at the same time")
- (PI4)  $at\alpha \rightarrow \neg nil$  if  $\alpha$  labels a statement  $a := t$   
 $at\alpha \wedge B \rightarrow \neg nil$  if  $\alpha$  labels a statement **await**  $B$  **then** ...  
("If some action is able to execute then some action has to execute")
- (PI5)  $at\alpha \wedge \neg \alpha \rightarrow \Box at\alpha$  ("An action ready to execute but not executed remains ready")
- (PI6)  $\alpha \wedge A_a(t) \rightarrow \Box A$  if  $\alpha$  labels a statement  $a := t$  or **await** ... **then**  $a := t$ ,  
 $A$  is a formula without temporal operators and without atomic formulas as defined above  
(describes the effect on  $\mu$  of executing  $\alpha$ )
- (PI7)  $\alpha_j^{(i)} \rightarrow \Box at\alpha_{j \oplus 1}^{(i)}$  for  $j = 0, \dots, m_i, i = 1, \dots, p$   
(describes the effect on  $R$  of executing  $\alpha_j^{(i)}$ )

Additional to this rule and axioms there should also be some *data axioms* describing the involved data.

### 3. Verification of programs

A *program property* is a property valid for every execution of the program. The temporal semantics leads to the possibility of formal verification of program properties  $A$  of a program  $\Pi$  by:

- describing  $A$  in  $\mathcal{L}_{T\Pi}$ ,
- deriving  $A$  in the formal system of first-order temporal logic augmented by rule (B1) and the other program axioms.

#### Examples of (typical) program properties

The intention of the sample program at the beginning of section 2 is that of a *producer/consumer* program: The first parallel component (the *producer*) cyclically "produces" in its statement list  $\alpha_{00}; \dots; \alpha_{0k_0}$  some object and stores it (in  $\alpha_{30}; \dots; \alpha_{3k_3}$ ) into a *buffer* shared with the other (*consumer*) component. The latter cyclically gets an object from the buffer (in  $\beta_{20}; \dots; \beta_{2l_2}$ ) and "consumes" it in  $\beta_{50}; \dots; \beta_{5l}$ . The synchronization is to achieve that

- the producer can only store an object when the buffer is not full,
- the consumer can only get an object when the buffer is not empty,
- producer and consumer must not have access to the buffer at the same time (this could be required by implementation details).

For formally describing some typical program properties by means of this example we introduce the following abbreviations:

$inPROD$  for  $at\alpha_{00} \vee \dots \vee at\alpha_{0k_0}$ ,  
 $inPUT$  for  $at\alpha_{30} \vee \dots \vee at\alpha_{3k_3}$ ,  
 $inCONS$  for  $at\beta_{50} \vee \dots \vee at\beta_{5l_5}$ ,  
 $inGET$  for  $at\beta_{20} \vee \dots \vee at\beta_{2l_2}$ .

E.g.,  $inPROD$  informally means that the producer is "in its produce section".

Suppose now,  $producedobj$  and  $consumedobj$  are program variables containing as values the objects which are handled in the cycles of the producer and the consumer, respectively. Then the following program properties could be of interest:

$start_{\Pi} \rightarrow producedobj=s$  before  $consumedobj=s$  (partial correctness)  
 $producedobj=s \rightarrow \diamond consumedobj=s$  (no loss property)  
 $start_{\Pi} \rightarrow \square \neg (inPUT \wedge inGET)$  (mutual exclusion of buffer access)  
 $start_{\Pi} \rightarrow \square (at\alpha_1 \wedge at\beta_0 \rightarrow be>0 \vee bf>0)$   
 $start_{\Pi} \rightarrow \square (at\alpha_1 \wedge at\beta_1 \rightarrow be>0 \vee ex=true)$   
 $start_{\Pi} \rightarrow \square (at\alpha_2 \wedge at\beta_0 \rightarrow ex=true \vee bf>0)$   
 $start_{\Pi} \rightarrow \square (at\alpha_2 \wedge at\beta_1 \rightarrow ex=true)$  } (deadlock freedom)  
 $at\alpha_1 \rightarrow \diamond at\alpha_{30}$   
 $at\beta_0 \rightarrow \diamond at\beta_{20}$  } (starvation freedom)  
 $(producedobj=s \wedge \diamond producedobj=s' \wedge s \neq s') \rightarrow$   
 $consumedobj=s$  before  $consumedobj=s'$  (FIFO-behaviour of the buffer)

### Verification rules

Program properties can be divided into three classes according to their syntactical form:

$A \rightarrow \square B:$	invariance properties	} safety properties
$A \rightarrow B$ atnext $C:$	} precedence properties	
$A \rightarrow B$ before $C:$		
$\vdots$		
$A \rightarrow \diamond B:$	liveness properties	

Basic proof principles for these kinds of properties can be derived from the corresponding purely logical proof rules together with some of the program axioms. In order to give an example, let us introduce the following abbreviations:

$B \text{ invof } \alpha$  for  $\alpha \wedge B \rightarrow \square B$  (" $B$  is an invariant of executing  $\alpha$ "),  
 $B \text{ invof } \mathfrak{M}$  for  $B \text{ invof } \alpha_1 \wedge \dots \wedge B \text{ invof } \alpha_k$  where  $\mathfrak{M} = \{\alpha_1, \dots, \alpha_k\}$ .

A basic proof rule for showing invariance properties is:

(inv)  $A \rightarrow B, B \text{ invof } \mathfrak{M}_{\Pi} \vdash A \rightarrow \square B$

It can be derived with the logical rule (ind $''$ ) as follows (suppose  $\mathfrak{M}_{\Pi} = \{\alpha_1, \dots, \alpha_k\}$ ):

- (1)  $A \rightarrow B$  assumption
- (2)  $\alpha_i \wedge B \rightarrow \square B$  for  $1 \leq i \leq k$  assumption
- (3)  $nil \wedge B \rightarrow \square B$  program axiom (B2)
- (4)  $\alpha_1 \vee \dots \vee \alpha_k \vee nil$  from definition of  $nil$
- (5)  $B \rightarrow \square B$  from (2), (3) and (4)
- (6)  $A \rightarrow \square B$  from (1) and (5) by (ind $''$ )

The only program axiom used in this justification of (inv) is (B2). Since (ind $''$ ) is a simple consequence of (ind), we may note this fact as:

(ind), (B2)  $\Rightarrow$  (inv).

In the same way we obtain rules (atnext), (before), ... for precedence properties:

(indatnext), (B2)  $\Rightarrow$  (atnext),  
 (indbefore), (B2)  $\Rightarrow$  (before),  
 $\vdots$

As an example, we note the rule

(before)  $\alpha \wedge A \rightarrow \square \neg C \wedge \square (A \wedge B)$  for every  $\alpha \in \mathfrak{M}_{\Pi}$ ,  $nil \wedge A \rightarrow \neg C \vdash A \rightarrow B$  before  $C$ .

For liveness properties the situation is similar in the result (that one obtains a verification rule from the logical rule (wfo)) but must be handled with some more care.

#### An example of a verification

As an example, we indicate the formal verification of deadlock freedom of the producer/consumer program  $\Pi$ .

- Proposition:** (1)  $start_{\Pi} \rightarrow \square (at\alpha_1 \wedge at\beta_0 \rightarrow be>0 \vee bf>0)$   
 (2)  $start_{\Pi} \rightarrow \square (at\alpha_1 \wedge at\beta_1 \rightarrow be>0 \vee ex=true)$   
 (3)  $start_{\Pi} \rightarrow \square (at\alpha_2 \wedge at\beta_0 \rightarrow ex=true \vee bf>0)$   
 (4)  $start_{\Pi} \rightarrow \square (at\alpha_2 \wedge at\beta_1 \rightarrow ex=true)$

We show: (5)  $start_{\Pi} \rightarrow \square \text{exor}(inPUT \vee at\alpha_4, inGET \vee at\beta_3, ex=true)$

$\text{exor}$  denotes the "exclusive or" of propositional logic, here applied to three formulas

$P, G, ex = true$  where

$P$  is  $inPUT \vee at\alpha_4$ ,  
 $G$  is  $inGET \vee at\beta_3$ .

Denote  $exor(P, G, ex = true)$  by  $B$ . (So (5) reads  $start_{\Pi} \rightarrow \square B$ .) We have:

(a)  $start_{\Pi} \rightarrow at\alpha_0 \wedge at\beta_0 \wedge ex = true$   
 $\rightarrow B$ ,

(b)  $\gamma \wedge B \rightarrow \square B$  for every  $\gamma \in \mathcal{M}_{\Pi}$ .

(a) is trivial; (b) has to be checked for all  $\gamma \in \mathcal{M}_{\Pi}$ . For example, let  $\gamma \in \{\alpha_{00}, \dots, \alpha_{0k_0}, \alpha_1, \alpha_5\}$ . Then  $\gamma \wedge B$  implies  $\neg P$  and also  $\square \neg P$ . The validity of  $G$  and  $ex = true$  remains unchanged under execution of  $\gamma$ , so we obtain  $\gamma \wedge B \rightarrow \square B$ . The other cases run similarly.

From (a) and (b) we get (5) by (inv), and from (5) we also can derive very easily the propositions (2), (3), (4). In order to prove (1) it can be shown in just the same way:

(6)  $start_{\Pi} \rightarrow \square (bf \geq 0 \wedge be \geq 0)$

and furthermore (using (6)):

(7)  $start_{\Pi} \rightarrow \square ((inPROD \vee at\alpha_1) \wedge (inCONS \vee at\beta_0) \rightarrow be > 0 \vee bf > 0)$ .

(1) is then very easily derived from (7) by temporal logic means.

#### 4. Axiomatic specification of programs

Program verification (with TL) means:

- Some program  $\Pi$  is given; the execution behaviour of  $\Pi$  is described in TL;
- program properties of  $\Pi$  are described in TL;
- the program properties are derived from the execution behaviour by the logical means of TL.

Another aspect is given by the keyword of (formal) program *specification*: This means (formally) describing the desired properties of a program which is to be constructed. We give a short introduction into a particular approach [2] to this field.

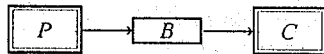
##### Abstract modules

Properties of parallel programs which are to be specified can be divided into two classes:

- Functionality properties (what is the program to do?),
- synchronization properties (what must be observed w.r.t. parallel execution?).

For illustration we consider the producer/consumer system of section 2. Before it was solved in the way given there, the problem could have been given, e.g., as follows.

- A parallel program is to be constructed containing two components  $P$  (producer) and  $C$  (consumer) using a shared buffer  $B$ :



- Functionality:

$P$  consists of cyclic loops producing objects and storing them into  $B$ ;  
 $C$  consists of cyclic loops of getting objects from  $B$  and consuming them;  
 $B$  is a complex object offering two operations:

- put an object into  $B$ ,
- get an object from  $B$ ;

The internal structure of  $B$  guarantees a ("queue"-like) *first-in-first-out* (FIFO) behaviour of putting and getting objects.

- Synchronization:

The access to  $B$  is mutually exclusive;  
 "put" is only possible if  $B$  is not full;  
 "get" is only possible if  $B$  is not empty.

This also shows that the synchronization given in the final program is actually forced by synchronization properties of the buffer operations "put" and "get". A formal framework for specifying the buffer  $B$  and its desired functionality and synchronization properties could be as follows:

```

module BUFFER(element): put, get
  variable b: queue
  procedures put(in: element),
             get(out: element)
  functions empty: → queue,
             top: queue → element,
             :
             isempty: queue → boolean,
             isfull: queue → boolean
  laws
  :
  axioms in a formal language  $\mathcal{L}(\text{BUFFER})$ 
  describing functionality and synchroni-
  zation properties of put and get
  :
endofmodule
  
```

} auxiliary functions  
(see below)

} (see below)

We call such a notation an *abstract module*. This module has the name BUFFER and depends on a "sort" element denoting the range of produced objects. For putting and getting objects, two procedures put (with an "input"-parameter of sort element) and get (with an "output"-parameter of this sort) are provided which are to work on the buffer realized by the variable  $b$ . The sort of  $b$  is denoted by queue. For specifying put and get, some auxiliary functions will be useful.

The computation model of such a module  $M$  is generally defined in the following way:

- At any time, procedures of  $M$  may be called from (parallel) program parts;
- a call of procedure  $f$  is "accepted" by  $M$  by creating a new instantiation of  $f$ ;
- before executing its "body",  $f$  may be forced to wait (for synchronization);
- there is a particular last action of  $f$  called termination;
- from instantiation to termination, the effect of  $f$  is given by a sequence of atomic actions which may be interleaved with other procedure calls.

##### An outline of the language $\mathcal{L}(M)$ for an abstract module $M$

$\mathcal{L}(M)$  is the language in which the specification of  $M$  (under the keyword **laws**) is to be carried out. We define  $\mathcal{L}(M)$  to be a first-order temporal language with

- the functions of  $M$  which are not boolean-valued as function symbols,
- the boolean-valued functions of  $M$  as predicate symbols.

Furthermore,  $\mathcal{L}(M)$  contains special atomic formulas (besides those of the form  $p(t_1, \dots, t_n)$  with predicate symbol  $p$  and terms  $t_1, \dots, t_n$ ). Let  $f$  be a module procedure of the general form

$$f(\text{in: sort}_1, \dots, \text{sort}_n, \text{out: sort}_{n+1}, \dots, \text{sort}_m)$$

Let  $\text{INS}(f) = \{f_0, f_1, \dots\}$  be a denumerable set of  $f$ -instantiation-symbols and  $(t_1, \dots, t_m)$  be the actual parameter list of  $f_i$ , i.e. the "argument list" of the call of  $f$  denoted by  $f_i$ . For  $f_i \in \bigcup_{f \in M} \text{INS}(f)$ , the following formulas (given with their informal meaning) are atomic formulas of  $\mathcal{L}(M)$ :

*init*: "M is in its initial state"  
*instf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: "The  $i$ -th call of the procedure  $f$  (i.e.  $f_i$ ) with parameters  $t_1, \dots, t_m$  is instantiated"  
*waitf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: " $f_i$  has been instantiated and waits for executing (its body)"  
*startf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: " $f_i$  starts to execute its body"  
*execf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: " $f_i$  executes some action of its body"  
*inf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: " $f_i$  is in its body"  
*termf<sub>i</sub>(t<sub>1</sub>, ..., t<sub>m</sub>)*: " $f_i$  terminates its execution"

**Example:** In the language  $\mathcal{L}(\text{BUFFER})$  we could write the formula

$$\text{startput}_i(e) \text{ before } \text{startput}_j(e')$$

with the informal meaning: "The execution of the body of the instantiation  $\text{put}_i(e)$  starts before the execution of the body of the instantiation  $\text{put}_j(e')$ ".

We still introduce an abbreviation:

$$\text{startf}_i(t_1, \dots, t_m) \rightarrow \text{new } y = h(y) \\ \text{for } \text{startf}_i(t_1, \dots, t_m) \wedge y = y_0 \rightarrow \square(\text{termf}_i(t_1, \dots, t_m) \rightarrow \circ(y = h(y_0)))$$

describing the effect of the call  $f_i(t_1, \dots, t_m)$ : "If  $f_i(t_1, \dots, t_m)$  starts executing (its body) with  $y$  having the value  $y_0$  then immediately after termination of this call,  $y$  will have the value  $h(y_0)$ ."

### Specification of a buffer

Finally we are able now to give the full specification of a buffer  $b$  as investigated above. The informal description is:

- $\text{put}(e)$  stores  $e$  in  $b$ ;
- $\text{get}(r)$  fetches an element from  $b$  on its out-parameter  $r$ ;
- $\text{put}$  and  $\text{get}$  are to terminate in finite time;
- order preserving: Objects are fetched from  $b$  in the same order as they are stored;
- priorities: Different instantiations of  $\text{put}$  are to be executed in a FIFO-manner (and the same for  $\text{get}$ );
- $\text{put}$  has the waiting condition that  $b$  is not full;  $\text{get}$  has the waiting condition that  $b$  is not empty;
- the bodies of different calls of  $\text{put}$  and/or  $\text{get}$  are to be mutually excluded;
- initially the buffer is empty.

The formal specification is given by the following abstract module:

**module** BUFFER(element):put,get

**variable**  $b$ :queue

**procédures** put(in:element),  
get(out:element)

**functions** empty:  $\rightarrow$ queue, (empty buffer)  
top: queue  $\rightarrow$ element, (top element of the buffer)  
rest: queue  $\rightarrow$ queue, (buffer without top element)  
append: queue  $\times$ element  $\rightarrow$ queue, (storing an element into the buffer)  
length: queue  $\rightarrow$ nat, (number of elements in the buffer)  
cap:  $\rightarrow$ nat, (capacity of the buffer)  
isempty: queue  $\rightarrow$ boolean, (test of being empty)  
isfull: queue  $\rightarrow$ boolean, (test of being full)

**laws** top(append(empty,e)) = e,  
top(append(q,e)) = top(q) if  $q \neq \text{empty}$ ,  
rest(append(empty,e)) = empty,  
rest(append(q,e)) = append(rest(q),e) if  $q \neq \text{empty}$ ,  
length(empty) = 0,  
length(append(q,e)) = length(q) + 1,  
cap  $\neq$  0,  
isempty(q)  $\leftrightarrow$  length(q) = 0,  
isfull(q)  $\leftrightarrow$  length(q) = cap,  
startput<sub>i</sub>(e)  $\rightarrow$  new  $b = \text{append}(b,e)$ ,  
startget<sub>i</sub>(r)  $\rightarrow$  new  $r = \text{top}(r)$ ,  
startput<sub>i</sub>(e)  $\rightarrow \diamond \text{termput}_i(e)$ ,  
startget<sub>i</sub>(r)  $\rightarrow \diamond \text{termget}_i(r)$ ,  
startput<sub>i</sub>(e)  $\rightarrow \neg \text{isfull}(b)$ ,  
startget<sub>i</sub>(r)  $\rightarrow \neg \text{isempty}(b)$ ,  
 $\neg(\text{input}_i(e) \wedge \text{input}_j(e'))$  for  $i \neq j$ ,  
 $\neg(\text{inget}_i(r) \wedge \text{inget}_j(r'))$  for  $i \neq j$ ,  
 $\neg(\text{input}_i(e) \wedge \text{inget}_j(r))$

} for realizing (d)

} (a),(b),(d)

} (c)

} (f)

} (g)

} (e)

} (h)

init  $\rightarrow b = \text{empty}$

**endofmodule**

### References

The material of sections 1-3 is taken from [1]. In that textbook there is also contained a detailed list of relevant literature.

- [1] F. Kröger: *Temporal logic of programs*. EATCS Monographs on Theoretical Computer Science. Springer-Verlag, Berlin, 1987
- [2] F. Kröger: *Abstract modules: Combining algebraic and temporal logic specification means*. Techn. Sci. Inform. 6, 559-572 (1987)
- [3] F. Kröger: *On the interpretation of arithmetic in temporal logic*. To appear in: Theor. Comp. Sci.